



2811

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Hutter et al.

Docket No.: TI-27203

Serial No.: 09/276,780 ✓

Examiner: Loke, S.

Filed: 03/25/99

Art Unit: 2811

For: MERGED BIPOLAR AND CMOS CIRCUIT AND METHOD

*6/9 Electronic
9/5/00*ELECTION

Assistant Commissioner for
Patents
Washington, DC 20231

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(A)
I hereby certify that the above correspondence is being deposited
with the U.S. Postal Service as first Class Mail in an envelope
addressed to Assistant Commissioner for Patents, Washington,
D.C. 20231 on:

August 24, 2000

Marianna Smith
Marianna Smith

Dear Sir:

With respect to the Restriction Requirement mailed on 08/01/00, the Examiner has restricted the instant application to the invention of Group I (Claims 6-11), or Group II (Claims 1-5). In light of this, Applicants elect to pursue Group II (Claims 1-5) without traverse.

Respectfully submitted,

Jacqueline J. Garner
Attorney for Applicant(s)
Reg. No. 36,144

Texas Instruments Incorporated
P.O. Box 655474, M/S 3999
Dallas, TX 75265
(972) 271-1176 or
(972) 917-5643
Fax: (972) 917-4418

RECEIVED
SEP - 5 2000
TC 2800 MAIL ROOM